Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (previously amended) A crosspoint switch integrated circuit comprising:

an array of input ports;

an array of output ports;

a switch matrix configured to selectively connect said input ports to said output ports for conducting electrical signals therebetween; and

equalization circuitry coupled to at least partially offset transmission losses experienced by said electrical signal while external to said crosspoint switch integrated circuit, said equalization circuitry being configured to measure jitter within said electrical signals and to utilize jitter measurements as a basis for offsetting said transmission losses, said equalization circuitry being responsive to said jitter measurements to automatically select levels of equalization.

2. (cancelled)

3. (original) The crosspoint switch integrated circuit of claim 1 wherein said equalization circuitry includes a plurality of adjustable equalizers, said adjustable equalizers each having adjustable filtering characteristics within a fixed number of equalization settings.

4. (original) The crosspoint switch integrated circuit of claim 3 wherein each said adjustable equalizer includes a plurality of switchable connections which individually adjust said filtering characteristics when activated.

5. (original) The crosspoint switch integrated circuit of claim 4 wherein each said switchable connection includes a switch, at least some of said switchable connections including at least one component which significantly affects said filtering characteristics when said switchable connections are individually activated.

6. (original) The crosspoint switch integrated circuit of claim 5 wherein at least some of said switchable connections are arranged in electrical parallel and said components include capacitors and resistors.

7. (original) The crosspoint switch integrated circuit of claim 5 wherein at least some of said switchable connections are arranged in electrical parallel and said components include an inductor and a resistor.

8. (original) The crosspoint switch integrated circuit of claim 5 wherein said switches are transistors and said components include at least some of resistors, capacitors, or inductors.

9. (original) The crosspoint switch integrated circuit of claim 4 wherein adjustable equalizers are coupled to said input ports in one-to-one correspondence.

10-18. (cancelled)

19. (currently amended) A method of providing equalization for a crosspoint switch formed on an integrated circuit chip ~~switch~~ comprising:

determining signal characteristics related to signal transmissions via each of a plurality of ports of said crosspoint switch, including providing on-chip measurements of jitter of electrical signals, wherein said jitter is induced by off-chip conditions; and

setting equalization circuitry housed within said crosspoint switch such that each said port has filtering characteristics tailored on a basis of said signal characteristics for said signal transmissions via said each port, said setting being automated and being at least partially based on said on-chip measurements of jitter.

20. (previously presented) The method of claim 19 wherein said step of setting includes selectively activating and deactivating switching devices which introduce parallel connections of resistances and capacitances within said equalization circuitry, said equalization circuitry being a plurality of adjustable equalization circuits.

21. (previously presented) The method of claim 19 wherein said step of setting includes selectively activating and deactivating switching devices which introduce series connections of resistances and inductances within said equalization circuits.

22. (original) The method of claim 19 wherein said step of setting includes activating adaptive equalization circuitry.

23-24. (cancelled)

25. (previously presented) The crosspoint switch integrated circuit of claim 1 wherein said equalization circuitry includes a multiplexer connected to a jitter measurement component for providing said jitter measurements, said multiplexer being connected to receive said electrical signals from each of said input ports and being operatively associated with said jitter measurement component to enable said jitter measurements on a port-by-port basis.

26. (previously presented) The crosspoint switch integrated circuit of claim 25 wherein said jitter measurement component includes a phase-locked loop for tracking data transactions within said electrical signals, said jitter measurement component further including a voltage-controlled oscillator connected to be responsive to operations of said phase-locked loop.

27. (previously presented) The crosspoint switch integrated circuit of claim 1 wherein said equalization circuitry is configured to recurringly execute said jitter measurements and recurringly execute responsive selection of said levels of equalization for individual said input ports, thereby enabling said levels of equalization to track variations in said transmission losses.